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concl¹⁰ previous digital input signal is synchronized with the clock frequency of the input signal (IN).

Please add new Claims 20-23:

ing⁵ 20. (New) A method for improving a quality of an output signal of an audio output stage which comprises at least a modulator circuit, the method comprising:

B2 comparing a signal generated in the audio output stage, which ³⁰³signal is proportional to a previous digital input signal, by means of feedback to the digital input signal of the audio output stage, in order to generate a digital control signal;

converting a digital output signal brought to the audio output ³⁰¹stage into a pulse train by means of modulation;

controlling an operation of the modulator circuit by means of the digital control signal, wherein the method further comprises:

amplifying the pulse train;
³⁰²

filtering the pulse train in the feedback circuit to a ³⁰³frequency range corresponding to the input signal;

converting the filtered pulse train into a digital signal in ³⁰⁴the feedback circuit,

adding bits to the digital signal so that the digital signal corresponds to the input digital word as regards the number of bits;

comparing the input digital word and the digital signal so as to produce a difference signal;

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Cont. determining a change data for the digital difference signal; and

on the basis of the change data the conversion of the input digital word of the output stage into a pulse train is controlled by means of a digital control signal.

21. (New) The method according to claim 20 wherein the modulation is performed using pulse density modulation.

22. (New) The method according to claim 20 wherein the modulation is performed using pulse width modulation.

Fig. 23. (New) A method for improving a quality of an output signal of an audio output stage which comprises at least a modulator circuit, the method comprising:

comparing, by means of feedback to a digital input signal of the audio output stage, a signal generated in the audio output stage, which signal is proportional to a previous digital input signal;